

## SPECIFICATION AMENDMENTS

Replace the paragraph beginning on line 17 of page 6 with the following:

As an example, in some embodiments of the invention, the circuit 298 may include a resistor divider 300 that is formed from resistors 301 that are serially coupled between a reference voltage (called  $V_{REF}$ ) and ground. The terminals of the resistors 301 provide reference voltages that the second stages 304 of the various DACs 62 use to furnish their analog signals based on the values that are stored in the respective memories 66. As an example, each second stage 304 may include a multiplexer 307 that has input terminals 308 that are coupled to receive indications of the bits from the SLM cells 50 of the unit 207. In this manner, each multiplexer 307 is associated with a different column and selects the bits from the memory ~~66~~ 60 of an SLM cell 50 of the selected row. The multiplexer 307 directs indications of these bits into a decoder 310. The decoder 310, in turn, operates switches 312 that receive the voltage across one of the resistors 301. The switches 312 furnish an analog voltage that is proportional to the value that is indicated by the bits, and an analog interface 314 scales this voltage before providing the voltage to a demultiplexer 316 that furnishes the scaled analog voltage to the appropriate capacitor 52. Thus, due to the above-described arrangement, each DAC 62 includes the resistor divider 300 (that forms the first stage) and the second stage 304.

Replace the title with the following:

--OPTICAL DISPLAY DEVICE HAVING A MEMORY TO ENHANCE REFRESH OPERATIONS--.